
Ese 1 Grade 1: Geografía - Eso

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Lecture notes with chapters of Geografia E Historia 1 Eso Santillana and exercises for the chapters. #2. CONELAB 02: Geografia E Historia 1 Eso Santillana -. Geografia E Historia 1 Eso Santillana -. Geografia E Historia 1 Eso Santillana -. Geografia E Historia 1 Eso Santillana -. Questionnaires

The subject matter disclosed herein relates to semiconductor devices and, more particularly, to a circuit for modeling high voltage characteristics of a semiconductor device, such as, a power semiconductor device, and a method of modeling the same. An IGBT (insulated gate bipolar transistor) and a MOSFET (metal-oxide-semiconductor field effect transistor) are generally used as a power semiconductor device. In particular, an IGBT is implemented as a power switch. An IGBT is one of power semiconductor devices to switch a load and is widely used in electric or electronic devices. An IGBT device includes a gate electrode and a main electrode. The gate electrode is insulated from the main electrode by a gate oxide layer. A control terminal is connected to the main electrode, and a collector terminal is connected to the main electrode to connect with a first end of a load. A second end of the load is connected to ground. The IGBT device has an n-type substrate formed of silicon. The substrate includes a p+ region formed of a n-type impurity ion implanted in a predetermined portion of the substrate. The substrate includes a p-type well formed of a n-type impurity ion implanted in a predetermined portion of the p+ region. An n-type buffer layer may be formed of a n-type impurity ion implanted at the predetermined portion of the substrate and at an upper surface of the p-type well and doped to a deeper region than the p-type well. The substrate further includes an n-type drift region formed of an n-type impurity ion implanted at the predetermined portion of the substrate and at the upper surface of the p-type well. A p-type drift region may be formed of a p-type impurity ion implanted at the predetermined portion of the substrate and at an upper surface of the n-type drift region. The substrate further includes a p+ emitter formed of a p-type impurity ion implanted at the predetermined portion of the substrate

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